EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	15	702/120.ccls. and (test$4 with controller$4) and register$4 and (input and output) and (bit with pattern$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/23 14:18
L2	98	"702"/$.ccls. and (test$4 with controller$4) and register$4 and (input and output) and (bit with pattern$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/23 14:18
L3	0	700/53.ccls. and (test$4 with controller$4) and register$4 and (input and output) and (bit with pattern$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/23 14:19
L4	6	710/1.ccls. and (test$4 with controller$4) and register$4 and (input and output) and (bit with pattern$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/23 14:19
L5	4	710/20.ccls. and (test$4 with controller$4) and register$4 and (input and output) and (bit with pattern$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/23 14:19
L6	5	710/52.ccls. and (test$4 with controller$4) and register$4 and (input and output) and (bit with pattern$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/23 14:19
L7	2	710/65.ccls. and (test$4 with controller$4) and register$4 and (input and output) and (bit with pattern$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/23 14:19
L8	119	"710"/$.ccls. and (test$4 with controller$4) and register$4 and (input and output) and (bit with pattern$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/23 14:20

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L9	64	714/30.ccls. and (test$4 with controller$4) and register$4 and (input and output) and (bit with pattern$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/23 14:20
L10	97	714/724.ccls. and (test$4 with controller$4) and register$4 and (input and output) and (bit with pattern$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/23 14:21
L11	94	714/726.ccls. and (test$4 with controller$4) and register$4 and (input and output) and (bit with pattern$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/23 14:21
L12	61	714/727.ccls. and (test$4 with controller$4) and register$4 and (input and output) and (bit with pattern$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/23 14:21
L13	23	714/729.ccls. and (test$4 with controller$4) and register$4 and (input and output) and (bit with pattern$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/23 14:21
L14	802	"714"/$.ccls. and (test$4 with controller$4) and register$4 and (input and output) and (bit with pattern$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/23 14:21
L15	204	14 and pin and block$4 and update$4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/23 14:23
S1	2	"20050268193"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/04/23 14:17

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S2	6	702/120.ccls. and (test$4 near controller$4) and register$4 and (input and output) and (bit near pattern$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/04/23 14:17
S3	0	700/53.ccls. and (test$4 near controller$4) and register$4 and (input and output) and (bit near pattern$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/20 17:16
S4	20	"702"/$.ccls. and (test$4 near controller$4) and register$4 and (input and output) and (bit near pattern$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/20 17:19
S5	0	710/1.ccls. and (test$4 near controller$4) and register$4 and (input and output) and (bit near pattern$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/20 17:19
S6	0	710/20.ccls. and (test$4 near controller$4) and register$4 and (input and output) and (bit near pattern$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/20 17:19
S7	0	710/52.ccls. and (test$4 near controller$4) and register$4 and (input and output) and (bit near pattern$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/20 17:19
S8	0	710/65.ccls. and (test$4 near controller$4) and register$4 and (input and output) and (bit near pattern$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/20 17:19
S9	8	"710"/$.ccls. and (test$4 near controller$4) and register$4 and (input and output) and (bit near pattern$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/20 17:21

S10	15	714/30.ccls. and (test$4 near controller$4) and register$4 and (input and output) and (bit near pattern$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/22 11:58
S11	155	"714"/$.ccls. and (test$4 near controller$4) and register$4 and (input and output) and (bit near pattern$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/20 17:25
S12	23	waayers.in.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/10/22 11:37
S13	15	714/724.ccls. and (test$4 near controller$4) and register$4 and (input and output) and (bit near pattern$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/22 11:59
S14	30	714/726.ccls. and (test$4 near controller$4) and register$4 and (input and output) and (bit near pattern$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/22 12:42
S15	15	714/727.ccls. and (test$4 near controller$4) and register$4 and (input and output) and (bit near pattern$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/22 13:04
S16	8	714/729.ccls. and (test$4 near controller$4) and register$4 and (input and output) and (bit near pattern$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/10/22 13:04